SWARNANDHR



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Approved by AICTE, New Delhi, Permanenet Affiliation to J N T U K, Kakinada Seetharampuram, NARSAPUR - 534 280, W.G.Dist., Andhra Pradesh

Date: 09-09-2019

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BOARD OF STUDIES-MINUTES OF MEETING FOR UG B.TECH PROGRAMME

Minutes of fourth Board Of Studies meeting of Department of Electronics & Communication Engineering, Swarnandhra College Of Engineering & Technology (Autonomous) on 09.09.2019 at 10:00a.m.

- 1. The Board Of Studies for Department of ECE for Swarnandhra College Of Engineering & Technology (Autonomous) is constituted with change of Internal members of SCET and made functional with the nominees of VC, JNTUK, Kakinada and Academic Council.
- 2. The new members of BOS have been introduced by the BOS Chairman Dr. B.S.Rao. to the other members.

UG PROGRAMME

BoS members are advised

- 1. To compare the present regulation course structure with R16 regulation of SCET and model course structures of AICTE, APSCHE.
- 2. To have prerequisites for each and every course and the same faculty have to deal the prerequisites and present course.
- 3. To have uniform distribution of courses, labs and credits among various semesters of the programme.
- 4. To follow the Semester wise course order in Electronics and Communication fields.
- 5. To shift the Internship from VI to VII semester, IPR from V to VII semester and Advanced Communication skills lab from V semester to VI semester iorder to balance the load in the course
- 6. To change the course name Analog Electronics-I in III semester as Electronic Circuits-I and Analog Electronics-II in the IV semester as Electronic Circuits-II.
- 7. To arrange the electives of R-19 Curriculum into 5 Baskets according to APSCHE and AICTE models as VLSI Design, Communication Engineering, Embedded Systems, Image processing, Computer Science and Engineering.
- 8. To balance the content of each unit and also mention the required hours for each Unit.
- 9. To balance the content of PDC, EDC and ECA properly in the syllabus of Electronic circuits-I and Electronic Circuits-II.
- 10. To include few Digital Transmitter and Receivers in the V unit of Analog and Digital Communication.

- 11. To have one or two preferred Text Books for every course related to units and to mention the number of units covered by the Text books in the Syllabus.
- 12. To permit Online Courses such as NPTEL in place of VIII semester Electives for the students who are placed in VII semester and unable to do the respective elective course in VIII semester in the campus.
- 13. To insist the students to do Internship minimum of 3 weeks to satisfy the credit allotment for internship in the course structure.
- 14. To include "Design Thinking and Product Design" in the open elective. They also added that more number of Guest lectures and workshops have to be arranged for the same.
- 15. To include only the content of Electron Devices in the course of Analog Electronics for EEE students.

S.NO.	Name of BOS Member	Designation in BOS	Signature
1	Dr.B.S Rao	Chairman	Buchan
2	Dr.B.T.Krishna	University Nominee	B.T. Kniel
3	Dr.N.N.Murthy	Subject Expert Nominated by Institute	Allen
4	Dr.K.Srirama Krishna	Subject Expert Nominated by Institute	ati
5	Dr.J.Sheeba Rani	Subject Expert Nominated by Institute	AMour
6	Mr.S.R.T.Rama Samy	Industrial Expert	f. Rectury
7	Mr.S.Naveen sri sai	Alumni Member	Shavenhill
8	Dr.A.Anto S K	Internal Member	South
9	Dr.N.K.Devi	Internal Member	Ogopomo
10	Mr.J.E.N. Abhilash	Internal Member	JZ NG AGKILL
11	Mr. D. Sekhar	Internal Member	(Serland
12	Mr. Y. V. A. Prasad	Internal Member	YVAPra
*13	Mr.R.Muralikrishna	Internal Member	Rule



BOARD OF STUDIES
Departement of ECE

warnandhra College of Engineering & Technology